TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

# TC74LCX04F,TC74LCX04FT,TC74LCX04FK

#### Low-Voltage Hex Inverter with 5-V Tolerant Inputs and Outputs

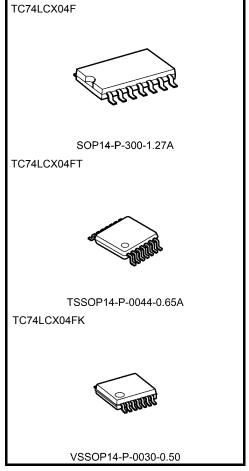
The TC74LCX04 is a high-performance CMOS inverter. Designed for use in 3.3-V systems, it achieves high-speed operation while maintaining the CMOS low power dissipation.

The device is designed for low-voltage (3.3 V) VCC applications, but it could be used to interface to 5-V supply environment for inputs

All inputs are equipped with protection circuits against static discharge.

#### **Features**

- Low-voltage operation: VCC = 1.65 to 3.6 V
- High-speed operation:  $t_{pd} = 5.2 \text{ ns (max) (V}_{CC} = 3.0 \text{ to } 3.6 \text{ V)}$
- Output current:  $|I_{OH}|/I_{OL} = 24 \text{ mA (min)} (V_{CC} = 3.0 \text{ V})$
- Latch-up performance: >±500 mA
- Available in JEITA SOP, TSSOP and VSSOP (US)
- · Power-down protection provided on all inputs and outputs
- Pin and function compatible with the 74 series (74AC/VHC/HC/F/ALS/LS etc.) 04 type

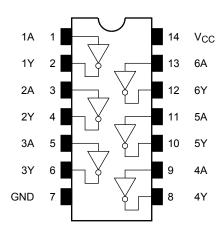


WWeight

SOP14-P-300-1.27A : 0.18 g (typ.) TSSOP14-P-0044-0.65A : 0.06 g (typ.) VSSOP14-P-0030-0.50 : 0.02 g (typ.)

Note: The Electrical Characteristics of  $V_{CC}=1.8\pm0.15V$  is only applicable for products which manufactured from January 2009 onward.

#### Pin Assignment (top view)



## **IEC Logic Symbol**

4.4.	1	4	<u> </u>	2	- 1Y
1A -	3	1		4	
2A -	5			6	- 2Y
3A -			$\vdash$		- 3Y
	9			8	- 4Y
4A -	11			10	
5A -	13		$\vdash$	12	5Y
6A -	13		$\overline{}$	12	6Y

#### **Truth Table**

Inputs	Outputs		
Α	Y		
L	Н		
Н	L		

## **Absolute Maximum Ratings (Note 1)**

Characteristics	Symbol	Rating	Unit
Power supply voltage	V <sub>CC</sub>	−0.5 to 7.0	V
DC input voltage	V <sub>IN</sub>	-0.5 to 7.0	٧
		-0.5 to 7.0 (Note 2)	V
DC output voltage	Vouт	-0.5 to V <sub>CC</sub> + 0.5 (Note 3)	
Input diode current	I <sub>IK</sub>	-50	mA
Output diode current	lok	±50 (Note 4)	mA
DC output current	lout	±50	mA
Power dissipation	PD	180	mW
DC V <sub>CC</sub> /ground current	I <sub>CC</sub> /I <sub>GND</sub>	±100	mA
Storage temperature	T <sub>stg</sub>	-65 to 150	°C

Note 1: Exceeding any of the absolute maximum ratings, even briefly, lead to deterioration in IC performance or even destruction.

Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 2:  $V_{CC} = 0 V$ 

Note 3: High or low state. IOUT absolute maximum rating must be observed.

Note 4:  $V_{OUT} < GND$ ,  $V_{OUT} > V_{CC}$ 



# **Operating Ranges (Note 1)**

Characteristics	Symbol	Rating	Unit
Power supply voltage	V <sub>CC</sub>	1.65 to 3.6	V
Fower supply voltage	vCC	1.5 to 3.6 (Note 2)	V
Input voltage	V <sub>IN</sub>	0 to 5.5	V
Output voltage	Vout	0 to 5.5 (Note 3)	V
Output voltage	VOU1	0 to V <sub>CC</sub> (Note 4)	V
Output current	I <sub>OH</sub> /I <sub>OI</sub>	±24 (Note 5)	mA
Output current	IOH/IOL	±12 (Note 6)	ША
Operating temperature	T <sub>opr</sub>	-40 to 85	°C
Input rise and fall time	dt/dv	0 to 10 (Note 7)	ns/V

Note 1: The operating ranges must be maintained to ensure the normal operation of the device. Unused inputs must be tied to either  $V_{CC}$  or GND.

Note 2: Data retention only

Note 3:  $V_{CC} = 0 V$ 

Note 4: High or low state ( However, it can not exceed I<sub>OUT</sub> of absolute maximum ratings. )

Note 5:  $V_{CC} = 3.0 \text{ to } 3.6 \text{ V}$ 

Note 6:  $V_{CC} = 2.7 \text{ to } 3.0 \text{ V}$ 

Note 7:  $V_{IN} = 0.8$  to 2.0 V,  $V_{CC} = 3.0$  V



## **Electrical Characteristics**

## DC Characteristics (Ta = -40 to 85°C)

Characteristics		Symbol Test Condition			Min	Max	Unit	
Sharaston			1000					<b>5</b> 1t
			_		1.65 to 2.3	$V_{CC} \times 0.9$	_	
	H-level	V <sub>IH</sub>			2.3 to 2.7	1.7	_	
lanut voltage					2.7 to 3.6	2.0	_	V
Input voltage					1.65 to 2.3	_	V <sub>CC</sub> × 0.1	V
	L-level	V <sub>IL</sub>		_	2.3 to 2.7	_	0.7	
					2.7 to 3.6	_	0.8	
				$I_{OH} = -100 \mu A$	1.65 to 3.6	V <sub>CC</sub> -0.2	_	
				I <sub>OH</sub> = -4 mA	1.65	1.05	_	V
	H-level	V <sub>ОН</sub>	$V_{IN} = V_{IL}$	$I_{OH} = -8 \text{ mA}$	2.3	1.7	_	
				I <sub>OH</sub> = -12 mA	2.7	2.2	_	
				I <sub>OH</sub> = -18 mA	3.0	2.4	_	
Output voltage				I <sub>OH</sub> = -24 mA	3.0	2.2	_	
Output voltage	L-level	V <sub>OL</sub>	$V_{IN} = V_{IH}$	$I_{OL} = 100  \mu A$	1.65 to 3.6	_	0.2	
				I <sub>OL</sub> = 4 mA	1.65	_	0.45	
				$V_{IN} = V_{IH}$	I <sub>OL</sub> = 8 mA	2.3	_	0.7
	L-ICVCI				I <sub>OL</sub> = 12 mA	2.7	_	0.4
				I <sub>OL</sub> = 16 mA	3.0	_	— 0.4	
				I <sub>OL</sub> = 24 mA	3.0	_	0.55	
Input leakage current		I <sub>IN</sub>	V <sub>IN</sub> = 0 to 5.5 V		1.65 to 3.6	_	±5.0	μΑ
Power-off leakage current		l <sub>OFF</sub>	V <sub>IN</sub> /V <sub>OUT</sub> =	5.5 V	0		10.0	μА
Quiescent supply current		I <sub>CC</sub>	V <sub>IN</sub> = V <sub>CC</sub> or GND		1.65 to 3.6		10.0	
Quioscont supply curre	J. I.	100	V <sub>IN</sub> = 3.6 to 5.5 V		1.65 to 3.6		±10.0	μΑ
Increase in Icc per inp	ut	$\Delta I_{CC}$	V <sub>IH</sub> = V <sub>CC</sub> - 0.6 V		2.7 to 3.6	_	500	



#### AC Characteristics ( $Ta = -40 \text{ to } 85^{\circ}\text{C}$ )

Characteristics	Symbol	Test Condition	V <sub>CC</sub> (V)	Min	Max	Unit
			$1.8 \pm 0.15$		20.0	no
Propagation delay time	t <sub>pLH</sub>	Figure 1, Figure 2	$2.5\pm0.2$		7.0	
Fropagation delay time	t <sub>pHL</sub>		2.7	_	6.0	ns
			$3.3 \pm 0.3$	1.5	5.2	
Output to output skew	t <sub>osLH</sub>	(Note)	2.7			ns
Output to output skew	t <sub>osHL</sub>	(Note)	$3.3 \pm 0.3$		1.0	113

Note: Parameter guaranteed by design.

 $(t_{OSLH} = |t_{pLHm} - t_{pLHn}|, t_{OSHL} = |t_{pHLm} - t_{pHLn}|)$ 

## Dynamic Switching Characteristics (Ta = 25°C, input: $t_r = t_f = 2.5$ ns, $C_L = 50$ pF, $R_L = 500$ $\Omega$ )

Characteristics	Symbol	Test Condition		Тур.	Unit
Quiet output maximum dynamic V <sub>OL</sub>	V <sub>OLP</sub>	$V_{IH} = 3.3 \text{ V}, V_{IL} = 0 \text{ V}$	3.3	0.8	V
Quiet output minimum dynamic V <sub>OL</sub>	V <sub>OLV</sub>	$V_{IH} = 3.3 \text{ V}, V_{IL} = 0 \text{ V}$	3.3	0.8	V

#### **Capacitive Characteristics (Ta = 25°C)**

Characteristics	Symbol	Test Condition	V <sub>CC</sub> (V)	Тур.	Unit
Input capacitance	C <sub>IN</sub>	_	3.3	7	pF
Output capacitance	C <sub>OUT</sub>	_	0	8	pF
Power dissipation capacitance	C <sub>PD</sub>	$f_{IN} = 10 \text{ MHz}$ (Note)	3.3	25	pF

Note: C<sub>PD</sub> is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

Average operating current can be obtained by the equation:

 $I_{CC \text{ (opr)}} = C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}/6 \text{ (per gate)}$ 

## **AC Test Circuit**

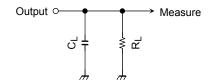


Figure 1

## **AC Waveform**

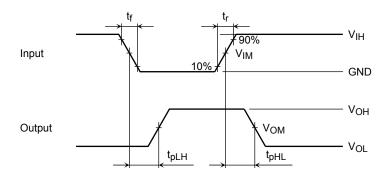


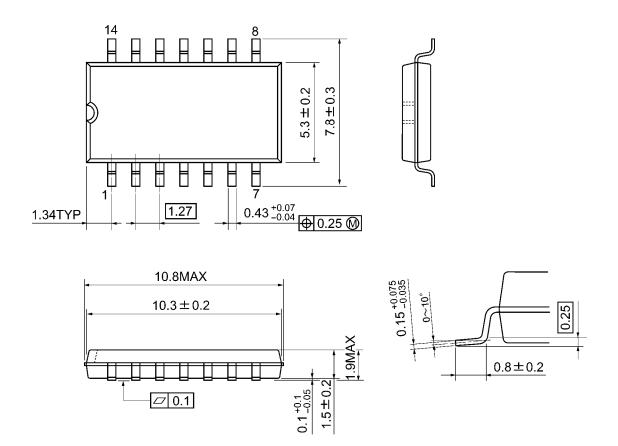
Figure 2 t<sub>pLH</sub>, t<sub>pHL</sub>

			Vcc					
	Symbol	$3.3 \pm 0.3 \text{ V}$ $2.7 \text{V}$	2.5 ± 0.2 V	1.8 ± 0.15 V				
Input	V <sub>IH</sub>	2.7V	V <sub>CC</sub>	V <sub>CC</sub>				
	V <sub>IM</sub>	1.5V	V <sub>CC</sub> /2	V <sub>CC</sub> /2				
	tr,tf	2.5ns	2.0ns	2.0ns				
Output	V <sub>OM</sub>	1.5V	V <sub>OH</sub> /2	V <sub>OH</sub> /2				
Load	CL	50pF	30pF	30pF				
	RL	500Ω	500Ω	1kΩ				

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# **Package Dimensions**

SOP14-P-300-1.27A Unit: mm



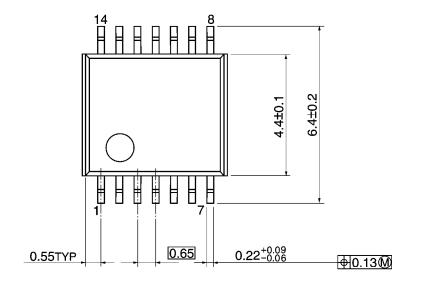
Weight: 0.18 g (typ.)

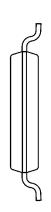
# **TOSHIBA**

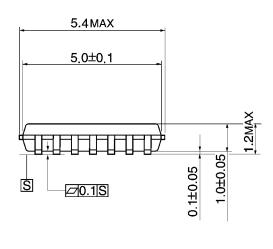
# **Package Dimensions**

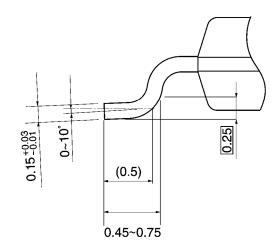
TSSOP14-P-0044-0.65A

Unit: mm







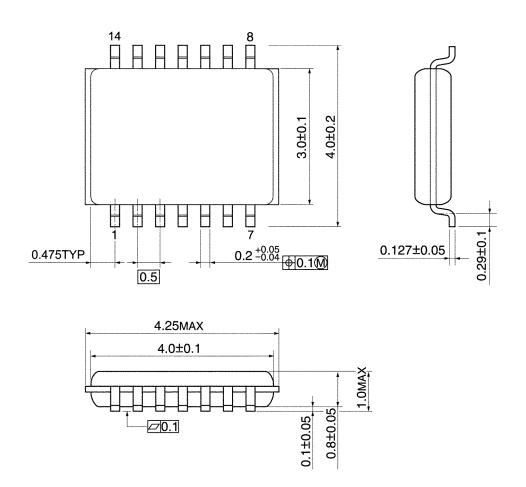


Weight: 0.06 g (typ.)



# **Package Dimensions**

VSSOP14-P-0030-0.50 Unit: mm



Weight: 0.02 g (typ.)

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